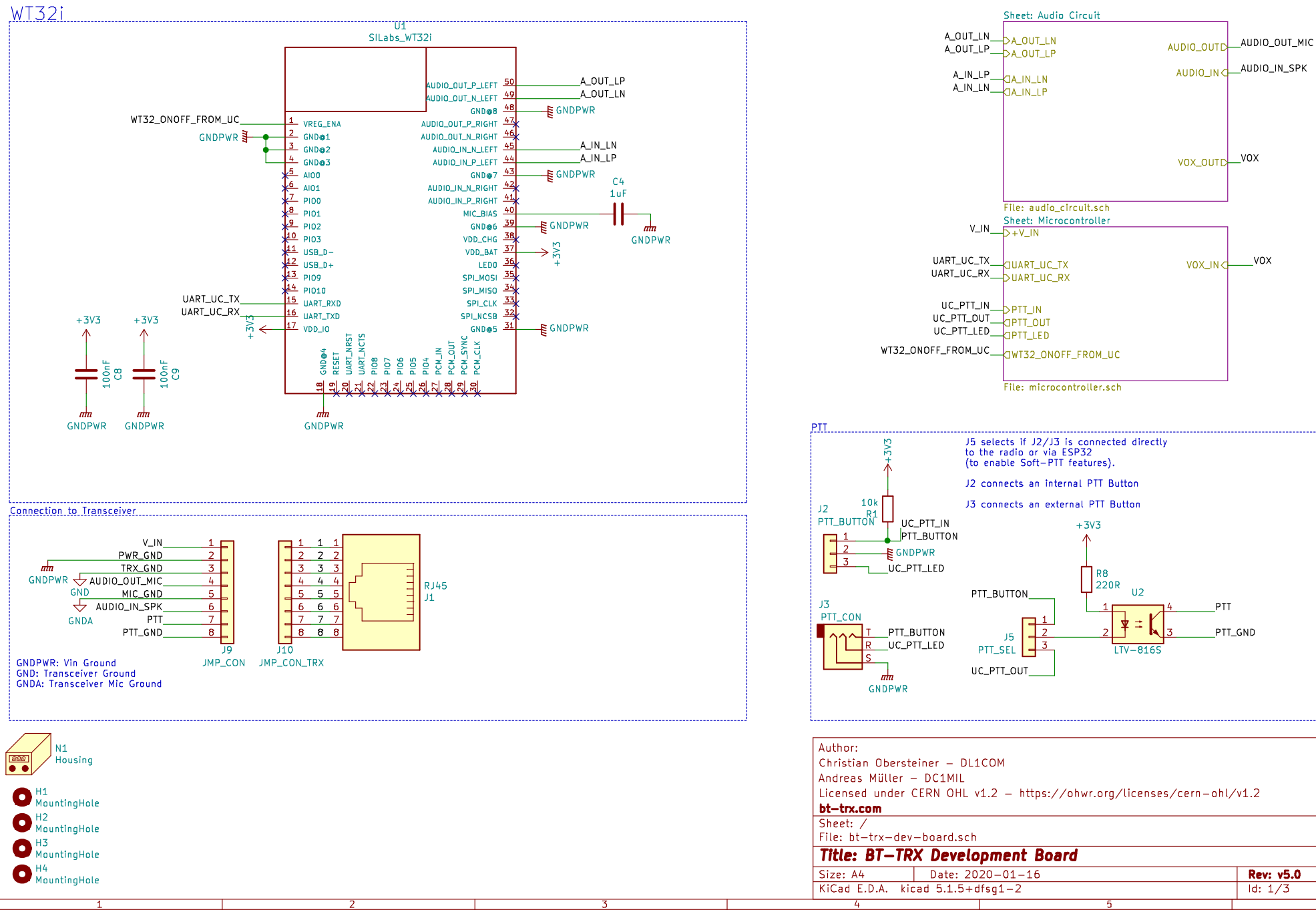
WT32i
U1
SILabs_WT32i
WT32_ONOFF_FROM_UC
GNDPWR
UART_UC_TX
UART_UC_RX
+3V3
A_OUT_LP
A_OUT_LN
A_IN_LN
A_IN_LP
C4
1uF
C8
100nF
C9
100nF
Sheet: Audio Circuit
A_OUT_LN
A_OUT_LP
A_IN_LP
A_IN_LN
AUDIO_OUT_MIC
AUDIO_IN_SPK
VOX
File: audio_circuit.sch
Sheet: Microcontroller
V_IN
UART_UC_TX
UART_UC_RX
UC_PTT_IN
UC_PTT_OUT
UC_PTT_LED
WT32_ONOFF_FROM_UC
VOX_IN
VOX
File: microcontroller.sch
Connection to Transceiver
V_IN
PWR_GND
TRX_GND
AUDIO_OUT_MIC
MIC_GND
AUDIO_IN_SPK
PTT
PTT_GND
GNDPWR
GND
GNDA
J9
JMP_CON
J10
JMP_CON_TRX
RJ45
J1
GNDPWR: Vin Ground
GND: Transceiver Ground
GNDA: Transceiver Mic Ground
PTT
J5 selects if J2/J3 is connected directly
to the radio or via ESP32
(to enable Soft-PTT features).
J2 connects an internal PTT Button
J3 connects an external PTT Button
J2
PTT_BUTTON
10k
R1
UC_PTT_IN
J3
PTT_CON
R8
220R
U2
LTV-816S
J5
PTT_SEL
PTT
PTT_GND
N1
Housing
H1
MountingHole
H2
MountingHole
H3
MountingHole
H4
MountingHole
Author:
Christian Obersteiner – DL1COM
Andreas Müller – DC1MIL
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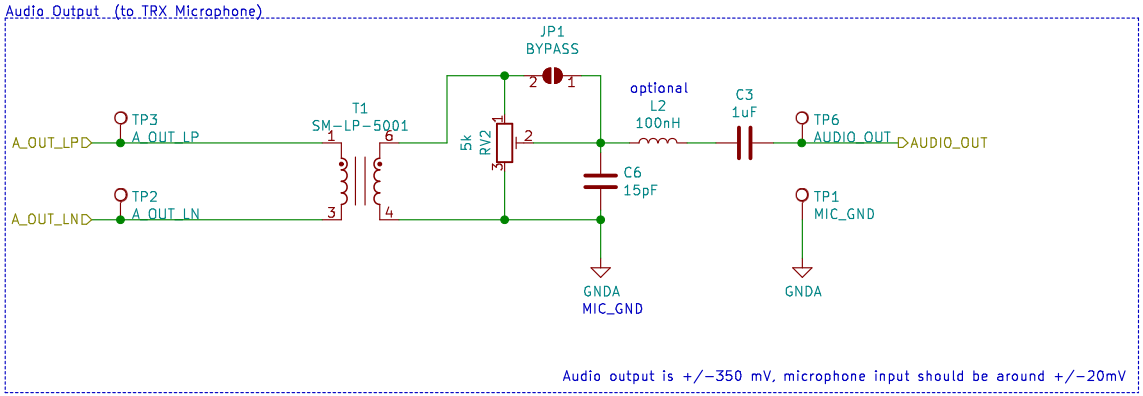
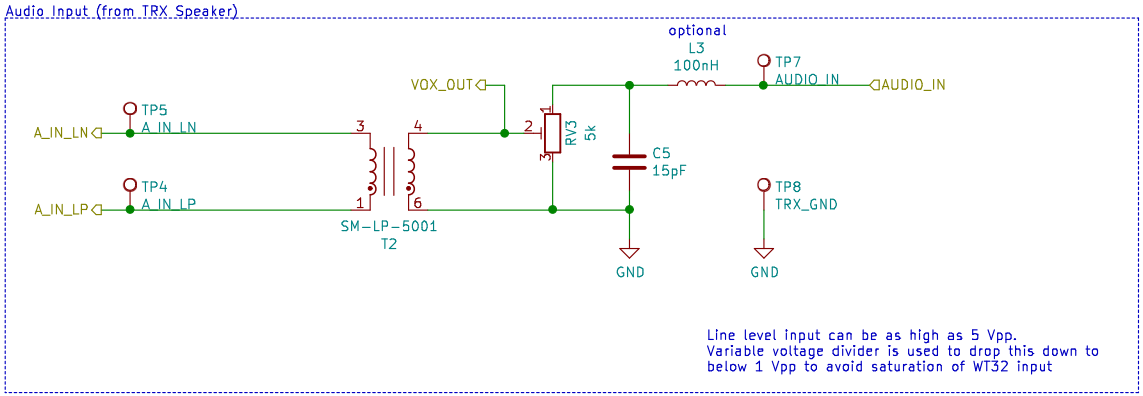
Sheet: /
File: bt-trx-dev-board.sch
Title: BT-TRX Development Board
Size: A4
Date: 2020-01-16
Rev: v5.0
KiCad E.D.A. kicad 5.1.5+dfsg1-2
Id: 1/3





Author:
Christian Obersteiner – DL1COM
Andreas Müller – DC1MIL

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Sheet: /Audio Circuit/
File: audio_circuit.sch

Title: BT-TRX Development Board

Size: A4	Date: 2020-01-16	**Rev: v5.0**
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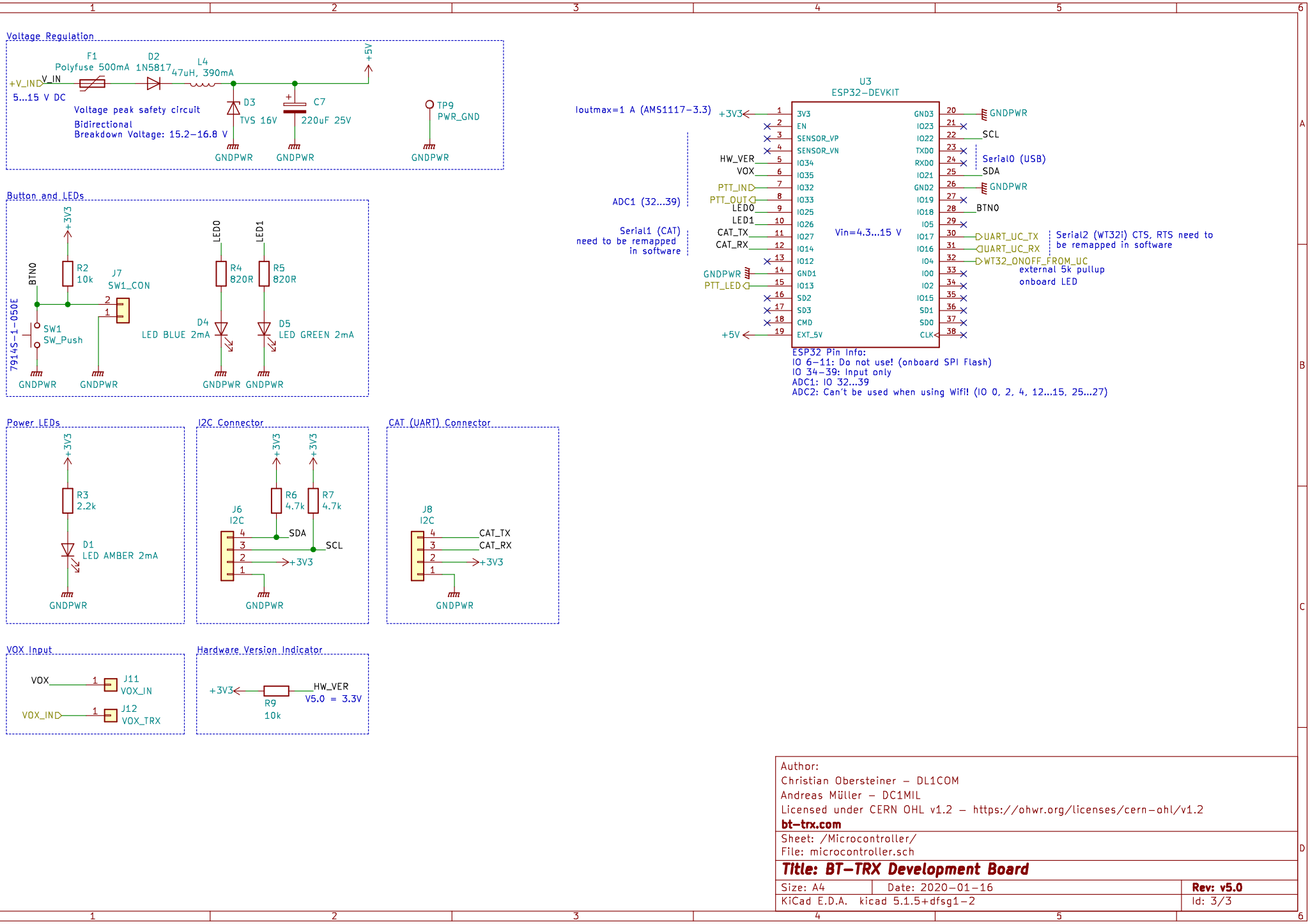

Author:
Christian Obersteiner – DL1COM
Andreas Müller – DC1MIL

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Sheet: /Microcontroller/
File: microcontroller.sch

Title: BT-TRX Development Board

Size: A4	Date: 2020-01-16	**Rev: v5.0**
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